

PATENT
81788.002510/EOT(3)
P. Walk
4-2-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Art Unit: 2814

FAX COPY RECEIVED

Seiichi Mori

Examiner: WEISS, H.

MAR 21 2002

Serial No.: 09/451,619

TECHNOLOGY CENTER 2800

Filed: November 30, 1999

For: NON-VOLATILE SEMICONDUCTOR
MEMORY HAVING A DECREASED GATE
LENGTH AND MANUFACTURING
METHOD THEREOF (As Amended)

I hereby certify that this correspondence is
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the United States Patent and Trademark
Office, Fax No. 703-308-7722 on March 21,
2002.

Sterlon R. Mason Reg. No. 41,179

Name

Signature

March 22, 2002

Date

REQUEST FOR EXTENSION OF TIMEAssistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In accordance with 37 C.F.R. § 1.136, Applicant respectfully petitions the Commissioner for a three-month extension of time extending from December 21, 2001 to March 21, 2002. The responsive papers are attached.

Please charge the fee of \$920.00 and any insufficiency or credit any overpayment to Deposit Account No. 50-1314.

Respectfully submitted,

HOGAN & HARTSON, L.L.P.

Date: March 22, 2002

By:

Sterlon R. Mason

Registration No. 41,179

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